

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	idle same critical same limit and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/12 13:09
S2	1	idle same critical same limit and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/12 13:09
S3	13	idle same critical and limit and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/12 13:10
S4	4	idle same critical same operation\$2 and limit and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/13 14:20
S6	177	idle same critical same operation\$2 same limit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:11
S8	21	idle same critical same operation\$2 same limit same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:33
S9	1	"idle state" same "critical state" same "limit state"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:15
S10	1	"idle state" same "critical state" same limit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:15
S11	1	idle same "critical state" same limit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:24
S12	149	idl\$3 same limit\$3 same critical same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:25
S13	16	idl\$3 same limit\$3 same critical same state same (substitut\$3 cop\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:26
S14	2	idl\$3 same limit\$3 same critical same state same substitutes	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:27
S15	3	idle same critical same operation\$2 same limit same detect\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:34
S16	29	idle same critical same detect\$1 same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:37
S17	408	idle same state same detect\$1 same memory	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 14:02

S18	10	idle same critical same state same detect\$1 same memory	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:48
S19	29	idle same critical same limit same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 13:48
S20	61	idle same state same detect\$1 same memory and critical and limit\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 14:06
S21	58	idle same state same detect\$1 same (transfer\$3 cop\$3 substitut\$3) and critical and limit\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 14:08
S22	437	idle same state same detect\$1 same (transfer\$3 cop\$3 substitut\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 14:24
S23	11	idle same limit same state same detect\$1 same (transfer\$3 cop\$3 substitut\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 15:28
S24	16	"nonvolatile memory" same state same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 15:30
S25	19	"nonvolatile memory" and (idle same (threshold limit\$3 predetermined) same state same detect\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 15:32
S26	70	idle same (threshold limit\$3 predetermined) same state same detect\$1 same memory	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 15:33
S27	19	idle same (threshold limit\$3 predetermined) same state same detect\$1 same memory and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 15:34
S28	4	idle same state same limit same detect\$1 same memory and 7\$/\$. ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 15:35
S29	182	idle same state same detect\$1 same (cop\$4 substitut\$4 transfer\$3) and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 15:51
S30	1	idle same critical same state same detect\$1 same (cop\$4 substitut\$4 transfer\$3 address\$3) and 7\$2/\$3. ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 17:12
S31	102	detect\$1 same (cop\$4 substitut\$4 transfer\$3 address\$3) same (error\$1 abnormal suspect) same idle and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 17:16
S32	4	detect\$1 same substitut\$4 same (error\$1 abnormal suspect) same idle and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/14 17:17

S33	28	7\$2/\$3.ccls. and detect\$1 same state same area same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 13:21
S34	27	7\$2/\$3.ccls. and detect\$1 near3 state same (area portion block) same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 13:23
S35	9	7\$2/\$3.ccls. and detect\$1 near3 state same (area portion block) and (idle same limit)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 13:29
S37	81	7\$2/\$3.ccls. and detect\$1 near3 state near3 (area block)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 13:59
S38	1	7\$2/\$3.ccls. and substitutes near3 area same idle same limit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 14:01
S39	1	7\$2/\$3.ccls. and substitutes near3 (area block portion) same idle same limit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 14:02
S40	5	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3) near3 (area block portion) same idle same limit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 14:03
S41	439	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3) same (area block portion) same idl\$3 same limit\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 14:06
S42	8	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3) same (area block portion) same idl\$3 same limit\$3 same critical	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 14:06
S43	12	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3) same (area\$1 block\$1 portion\$1) same idl\$3 same limit\$3 same critical	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 14:09
S44	3364	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3) same (area\$1 block\$1 portion\$1) same idl\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 14:10
S45	606	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3) same (area\$1 block\$1 portion\$1) same idl\$3 same limit\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 15:31
S46	12	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3) same (area\$1 block\$1 portion\$1) same idl\$3 same limit\$3 same critical	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 14:17

S47	12	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same idl\$3 same "limit state"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 16:24
S48	10	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same idl\$3 same limit\$1 same critical	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 16:27
S49	18	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same idl\$3 same critical same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 16:33
S50	496	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same critical same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 16:38
S51	33	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same "critical state"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:03
S52	12	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same "limit state"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:07
S53	179	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same "limit value"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 14:14
S54	4	7\$2/\$3.ccls. and detect\$3.ti. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same "limit value"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:11
S55	2	7\$2/\$3.ccls. and detect\$3.ti. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same limit same critical	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:15
S56	207	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4 re-address\$4 readdress\$4) same (area\$1 block\$1 portion\$1) same limit same critical	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:16

S57	7	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 map\$4 re-map\$3 remap\$3 cop\$3 address\$4 re-address\$4 readdress\$4) same (area\$1 block\$1 portion\$1) same limit same critical same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:32
S58	250	limit same critical same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:33
S59	11	limit same critical same idle same memory	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:33
S60	1	limit and "critical state" and "idle state" and memory.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:34
S61	16	limit and "critical state" and "idle state"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:34
S62	9	limit and "critical state" and "idle state" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:35
S63	28	limit and "critical state" and idle and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 17:39
S64	58	limit and "critical state" and idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 18:06
S65	114	"limit value" and critical and idle and semiconductor	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 18:11
S66	275	"limit value" and critical and idle and memory	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/15 18:11
S67	97	"limit value" and critical and idle and memory and substitut\$4	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 13:48
S69	390	limit and critical and idle and (substitut\$4 remap\$3 re-map\$3) same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 13:50
S70	1	("limit state" "limit status" "limit value") and ("critical state" "critical status" "critical value") and ("idle state" "idle status") and (substitut\$4 remap\$3 re-map\$3) same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 13:52

S71	9	("limit state" "limit status" "limit value") and ("critical state" "critical status" "critical value") and ("idle state" "idle status") and (substitut\$4 remap\$3 re-map\$3) same memory	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 13:52
S72	920	7\$2/\$3.ccls. and (substitut\$3 transfer\$3 re-map\$3 remap\$3 cop\$3 address\$4) same (area\$1 block\$1 portion\$1) same ("limit value" "limit status" "limit state" "critical status" "critical state" "idle status" "idle state")	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 14:18
S73	2	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3) same (area\$1 block\$1 portion\$1) same ("limit value" "limit status" "limit state" "critical status" "critical state" "idle status" "idle state") and (non-volatile nonvolatile)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 14:19
S74	28	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3) same (area\$1 block\$1 portion\$1) same ("limit value" "limit status" "limit state" "critical status" "critical state" "idle status" "idle state")	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 14:20
S75	134	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3 cop\$3) same (area\$1 block\$1 portion\$1) same ("limit value" "limit status" "limit state" "critical status" "critical state" "idle status" "idle state")	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 14:30
S76	186	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3 cop\$3) same (area\$1 block\$1 portion\$1 memory) same ("limit value" "limit status" "limit state" "critical status" "critical state" "idle status" "idle state")	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 15:34
S77	1463	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3 cop\$3 backup) same (area\$1 block\$1 portion\$1 memory) same semiconductor	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 15:39
S78	4239	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3 cop\$3 backup) same (area\$1 block\$1 portion\$1 memory) same semiconductor and nonvolatile.ti. non-volatile.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 15:40
S79	4	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3 cop\$3 backup) same (area\$1 block\$1 portion\$1 memory) same semiconductor same state and (non-volatile).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 15:45

S80	0	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3 cop\$3 backup) same (area\$1 block\$1 portion\$1 memory) same semiconductor same state and (non-volatile).ti. and (critical same limit)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 15:46
S81	3	7\$2/\$3.ccls. and (substitut\$3 re-map\$3 remap\$3 cop\$3 backup) same (area\$1 block\$1 portion\$1 memory) same state and (non-volatile).ti. and (critical same limit)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 16:00
S82	1	7\$2/\$3.ccls. and substitut\$3 same (area\$1 block\$1 portion\$1 memory) same state and (non-volatile).ti. and (critical same limit)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 16:02
S83	2	7\$2/\$3.ccls. and substitut\$3 same (area\$1 block\$1 portion\$1 memory) same state and (non-volatile).ti. and ((critical suspect error) same limit\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 16:04
S84	8	7\$2/\$3.ccls. and substitut\$3 same (area\$1 block\$1 portion\$1 memory) same state and (non-volatile).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/16 16:11
S85	8	7\$2/\$3.ccls. and substitut\$3 same memory same state and (non-volatile).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 10:45
S86	58	7\$2/\$3.ccls. and memory same (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) and (non-volatile).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 12:25
S87	4997	7\$2/\$3.ccls. and memory same (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same process\$4	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 12:27
S88	36	7\$2/\$3.ccls. and memory same (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same process\$4 same writ\$3 same critical	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 13:13
S89	57	7\$2/\$3.ccls. and (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same process\$4 same writ\$3 same critical	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 14:57
S90	1	7\$2/\$3.ccls. and (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same process\$4 same writ\$3 same critical same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 14:59

S91	117	7\$2/\$3.ccls. and (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same process\$4 same writ\$3 same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 15:02
S92	6	7\$2/\$3.ccls. and (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same process\$4 same writ\$3 same idle same predetermined	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 15:44
S94	4	7\$2/\$3.ccls. and (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same process\$4 same writ\$3 same idle same (abnormal\$4 fault\$2)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 15:48
S95	14	7\$2/\$3.ccls. and (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same writ\$4 same idle same (abnormal\$4 fault\$2)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 15:49
S96	38	7\$2/\$3.ccls. and (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same writ\$4 same idle same (abnormal\$4 fault\$2 erro\$5)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 16:09
S97	1	7\$2/\$3.ccls. and (state status) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same writ\$4 same limit same critical same (abnormal\$4 fault\$2 erro\$5)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 16:10
S10 0	288	7\$2/\$3.ccls. and (critical same limit) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 16:14
S10 1	5	7\$2/\$3.ccls. and (critical same limit) same (swap\$4 remap\$4 re-map\$4 cop\$4 substitut\$4) same (nonvolatile non-volatile)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/19 17:08
S10 2	1	7\$2/\$3.ccls. and "successive retry error"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/20 11:45
S10 3	162	7\$2/\$3.ccls. and "retry error"	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/20 11:48
S10 5	68	7\$2/\$3.ccls. and "retry error" same (state status)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/20 11:50
S10 6	22	7\$2/\$3.ccls. and (insufficie\$4 near3 free) same error	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/20 11:53
S10 7	2	7\$2/\$3.ccls. and (insufficie\$4 near3 free) same (state status) same (critical limit)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/20 11:56

S108	30	7\$2/\$3.ccls. and (insufficie\$4 near3 free) same (state status)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/20 12:33
S109	1	("6,363,502").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 15:51
S110	973	semiconductor same (remap\$4 re-map\$4 substitut\$4) same state	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 15:55
S111	26	semiconductor same (remap\$4 re-map\$4 substitut\$4) same (detect\$4 near2 (state status))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:08
S112	45	semiconductor same memory and (remap\$4 re-map\$4 substitut\$4) same (detect\$4 near2 (state status))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:10
S113	1	semiconductor same memory and (remap\$4 re-map\$4 substitut\$4 copies copy) same (detect\$4 near2 (state status)) same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:10
S114	1	semiconductor same memory and (remap\$4 re-map\$4 substitut\$4 cop\$4) same (detect\$4 near2 (state status)) same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:10
S115	99	semiconductor same memory and (remap\$4 re-map\$4 substitut\$4 cop\$4) same (state status) same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:15
S116	20	semiconductor same memory and (remap\$4 re-map\$4 substitut\$4 cop\$4) same (state status) same idle same (error\$4 fault\$1 abnormal)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:14
S117	1	7\$2/\$3.ccls. and semiconductor same memory and (remap\$4 re-map\$4 substitut\$4 cop\$4) same (state status) same idle same (limit\$3 critical) same (erro\$5 fault\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:17
S118	18	7\$2/\$3.ccls. and semiconductor same memory and (remap\$4 re-map\$4 substitut\$4 cop\$4) same (state status) same idle same (erro\$5 fault\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:22
S119	2	7\$2/\$3.ccls. and semiconductor same memory and (remap\$4 re-map\$4 substitut\$4 cop\$4) same (state status) same (idle and (critical limit)) same (erro\$5 fault\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:23

S12 0	11	7\$2/\$3.ccls. and semiconductor same memory and (re-map\$4 re-map\$4 substitut\$4 cop\$4 swap\$4) same (state status) same (idle and (critical limit))	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:31
S12 2	194	7\$2/\$3.ccls. and semiconductor same memory and (re-map\$4 re-map\$4 substitut\$4 cop\$4 swap\$4) same (detect\$4 determin\$4) near3 (state status)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:42
S12 3	254	7\$2/\$3.ccls. and semiconductor same memor\$4 and (re-map\$4 re-map\$4 substitut\$4 cop\$4 swap\$4) same (detect\$4 determin\$4) near4 (state status)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:51
S12 4	2	7\$2/\$3.ccls. and semiconductor same memor\$4 and (re-map\$4 re-map\$4 substitut\$4 cop\$4 swap\$4) same (detect\$4 determin\$4) near4 (state status) and (idle same critical same limit\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:53
S12 5	1	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) same (idle same critical same limit\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:54
S12 6	550	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) and (critical same limit\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:54
S12 7	4	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) and (idle same critical same limit\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:55
S12 8	31	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) same (idle and critical and limit\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 16:57
S12 9	96	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) same (critical and limit)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 17:04
S13 0	3	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) same (critical and limit) same (correct\$4 fix\$4 repair\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 17:05
S13 1	102	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) same (erro\$5 fault\$3 abnormal) same (correct\$4 fix\$4 repair\$4) same (re-map\$4 re-map\$4 substitut\$4 cop\$4 swap\$4)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 17:15

S13 2	2	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) same (erro\$5 fault\$3 abnormal) same (correct\$4 fix\$4 repair\$4) same (remap\$4 re-map\$4 substitut\$4 cop\$4 swap\$4) same idle	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 17:27
S13 3	6	7\$2/\$3.ccls. and semiconductor same memor\$4 and (state status) same (erro\$5 fault\$3 abnormal) same (correct\$4 fix\$4 repair\$4) same (remap\$4 re-map\$4 substitut\$4 cop\$4 swap\$4) same (limit threshold)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/21 17:27